

Docket No. BUR920030184US1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

DeVries, et al.

Serial No.: 10/709,325

Group Art Unit: Unknown

Filed: April 28, 2004

Examiner: Unknown

For: METHOD AND STRUCTURE FOR CONNECTING GROUND/POWER NETWORKS TO PREVENT CHARGE DAMAGE IN SILICON ON INSULATOR

Honorable Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUBMISSION OF REPLACEMENT FORMAL DRAWINGS**

Sir:

Submitted herewith are six (6) sheets of replacement formal drawings comprising Figures 1 - 9 for the above-identified patent application. Please substitute these replacement formal drawings for the formal drawings that were filed with the application.

Approval and acknowledgment of receipt of the drawings are respectfully requested.

Respectfully submitted,

The signature of Frederick E. Cooperrider, Esq., is written in cursive ink. It appears to read "Frederick E. Cooperrider". Below the signature, the name "Frederick E. Cooperrider, Esq." is printed in a smaller, sans-serif font, followed by "Registration No. 36,769".

Date: 6/2/04

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